



Herguan University

595 Lawrence Expressway

Sunnyvale, CA 94085

TEL: 408.481.9988 | FAX: 408.745.7580

Summer Term 2009

SCHEDULE OF CLASSES FOR MASTER OF COMPUTER SCIENCE FROM 05/11/2009 -08/23/2009

DAY	COURSE NO	COURSE TITLE	UNITS	TIME	INSTRUCTOR	PRE-REQ	ROOM NO
MON	CS 545	UNIX/LINUX NETWORK PROGRAMMING	3	6:30PM-9:30PM	Dr. John Liu	CS 430	102
	CS 536	OPERATING SYSTEM DESIGN	3	6:30PM-9:30PM	Dr. Charles Lee	CS 408	202
TUE	CS 562	ADVANCED JAVA PROGRAMMING	3	6:30PM-9:30PM	Dr. Zhou Min	CS 530	102
	CS 517	DATABASE DESIGN	3	6:30PM-9:30PM	Mr. Ansjory	CS 404	202
THU	CS 501	COMPUTER ARCHITECTURE	3	6:30PM-9:30PM	Dr. Zhou Min	Instructor's Consent	102
SAT	CS 530	JAVA PROGRAMMING AND INTERNET APPLICATIONS	3	10:00PM-1:00PM	Dr. Robert Zhu	CS 460	202
	CS 577	ADVANCED DATABASE DESIGN AND DEVELOPMENT	3	10:00AM-1:00PM	Dr. Simon Luo	CS 517	102
	CS 520	COMPUTER NETWORKS	3	2:00PM-5:00PM	Dr. Simon Luo	CS 480	102
	CS 597	MASTER PROJECT	3		Dr. Robert Zhu Dr. Zhou Min	Dr. Simon Luo Dr. John Liu	Dr. Charles Lee Dr. Ansjory
	CS 395A	INTERNSHIP 1	3				
	CS 395B	INTERNSHIP 2	3				
	CS 395C	INTERNSHIP 3	3				
	CS 497A	SENIOR DESIGN PROJECT 1	3			elective	
	CS 497B	SENIOR DESIGN PROJECT 2	3			elective	
	CS 599A	MASTERS THESIS 1	3			elective	
	CS 599B	MASTERS THESIS 2	3			elective	

**NOTE: Any student who misses three or more classes for each course will be automatically dropped from the course
Any late on attendance will be count as ½ missing class and the class limit is up to 60 students only.**



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SCHEDULE OF CLASSES FOR MASTER OF BUSINESS ADMINISTRATION FROM 05/11/2009 -08/23/2009

DAY	COURSE NO	COURSE TITLE	UNITS	TIME	INSTRUCTOR	PRE-REQ	ROOM NO
MON	MGT 511	ORGANIZATIONAL BEHAVIOR	3	6:00PM-9:00PM	Dr. Ryan Baidya	Instructor's Consent	101
TUE	BUS 510	QUANTITATIVE METHOD FOR BUSINESS	3	3:00PM-6:00PM	Dr. Dalili Fred		101
	MGT 515	SUPPLY CHAIN MANAGEMENT FOR E-BUSINESS	3	6:00PM-9:00PM	Dr. Ryan Baidya	MGT 506	101
THU	MHCM 506	CONTINUOUS QUALITY IMPROVEMENT IN HEALTH SERVICES	3	6:00PM-9:00PM	Dr. Ann Shin		101
FRI	MGT 501	HUMAN RESOURCES MANAGEMENT	3	5:30PM-8:30PM	Dr. Zhou Min	Instructor's Consent	102
	MBA 598	MASTER PROJECT	3		Dr. Dalili Fred	Dr. Ryan Baidya	Dr. Samuel Tong
	MHCM530	MASTER PROJECT	3		Dr. Ann Shin		
	MBA 395A	INTERNSHIP 1	3				
	MBA 395B	INTERNSHIP 2	3				
	MBA 395C	INTERNSHIP 3	3				
	MBA 497A	SENIOR DESIGN PROJECT 1	3			elective	
	MBA 497B	SENIOR DESIGN PROJECT 2	3			elective	
	MBA 599A	MASTERS THESIS 1	3			elective	
	MBA 599B	MASTERS THESIS 2	3			elective	

NOTE: Any student who misses three or more classes for each course will be automatically dropped from the course. Any late on attendance will be count as ½ missing class and the class limit is up to 60 students only. Each course should have amount 8 or over 8 students in one class, so school can open the course.



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SCHEDULE OF CLASSES FOR MASTER OF SCIENCE IN ELECTRICAL ENGINEERING FROM 05/11/2009 -08/23/2009

DAY	COURSE NO	COURSE TITLE	UNITS	TIME	INSTRUCTOR	PRE-REQ	ROOM NO
SAT	EE 515	DIGITAL IC DESIGN	3	10:00am-1:00pm	Dr. Sateesh Gudla	EE 440	101
SAT	EE 548	VLSI PHYSICAL DESIGN-PLACE AND ROUTE	3	2:00pm-5:00pm	Dr. Sateesh Gudla	Senior standing	101
SAT	EE 471	VERILOG HDL AND DIGITAL DESIGN	3	5:15pm-8:15pm	Dr. Jack C Ho	EE 440	101
	EE 597	MASTER PROJECT	3		Dr. Sateesh Gudla		
	EE 395A	INTERNSHIP 1	3				
	EE 395B	INTERNSHIP 2	3				
	EE 395C	INTERNSHIP 3	3				
	EE 497A	SENIOR DESIGN PROJECT 1	3			elective	
	EE 497B	SENIOR DESIGN PROJECT 2	3			elective	
	EE 599A	MASTERS THESIS 1	3			elective	
	EE 599B	MASTERS THESIS 2	3			elective	

NOTE: Any student who misses three or more classes for each course will be automatically dropped from the course
 Any late on attendance will be count as $\frac{1}{2}$ missing class and the class limit is up to 60 students only.
 Each course should have amount 8 or over 8 students in one class, so school can open the course.